

REMARKS/ARGUMENTS

Formal drawings for this application were filed in the USPTO on June 2, 2004. Applicant respectfully requests the Examiner to give approval of the filed formal drawings.

Various claims are being amended as shown above. The claim amendments clarify the claim language and are not intended to limit the scope of the claims, unless the claim language is expressly quoted in the following remarks to distinguish over the cited art.

In section 3 of the office action, claims 13, 16 and 18-20 were rejected under 35 U.S.C. 102(b) as allegedly being anticipated by Jodoin (U.S. Patent No. 4,636,918). Applicant respectfully traverses the rejection.

Jodoin is directed to an integrated circuit 10" and capacitor 20" both mounted on a printed circuit board 14", where the leads 28" of the capacitor 20" will shared the same through-holes 16" (in the board 14") as the ground/power leads of the integrated circuit 10". Jodoin does not disclose the printed circuit board as having a second electrical contact area that is used for In-Circuit Testing, as substantially recited in claim 13. Jodoin also does not disclose in connecting a capacitor to a second electrical contact area of a substrate after In-Circuit Testing through the second electrical contact area has been performed on the substrate while an electrical component is connected to the substrate, as substantially recited in claim 13. Accordingly, claim 13 is patentable over Jodoin.

Claims 16 and 18-20 each depends from claim 13, and are each patentable over Jodoin for at least the same reasons that claim 13 is patentable over Jodoin.

Furthermore, each of the claims 16 and 18-20 distinguishes over Jodoin by reciting additional features. Accordingly, claims 16 and 18-20 are each patentable over Jodoin.

For the above reasons, Applicant requests reconsideration and withdrawal of the rejection under 35 U.S.C. §102.

In section 4 of the office action, claims 13 and 16-20 were rejected under 35 U.S.C. 102(e) as allegedly being anticipated Kozak, et al. (U.S. Patent No. 6,414,850). Applicant respectfully traverses the rejection.

Kozak is directed to a main circuit board 114 with a mounted small circuit board 412 having capacitance and with a mounted integrated circuit package 112. Kozak does not disclose the main circuit board as having a second electrical contact area that is used for In-Circuit Testing, as substantially recited in claim 13. Kozak also does not disclose in connecting a capacitor to a second electrical contact area of a substrate after In-Circuit Testing through the second electrical contact area has been performed on the substrate while an electrical component is connected to the substrate, as substantially recited in claim 13. Accordingly, claim 13 is patentable over Kozak.

Claims 16-20 each depends from claim 13, and are each patentable over Kozak for at least the same reasons that claim 13 is patentable over Kozak. Furthermore, each of the claims 16-20 distinguishes over Kozak by reciting additional features. Accordingly, claims 16-20 are each patentable over Kozak.

For the above reasons, Applicant requests reconsideration and withdrawal of the rejection under 35 U.S.C. §102.

In section 6 of the office action, claims 14-15 were rejected under 35 U.S.C. 103(a) as allegedly being unpatentable over Kozak or Jodoin in view of Kabadi (U.S. Patent No. 6,097,609), and further in view of Wisser (U.S. Patent No. 3,721,941). Applicant respectfully traverses the rejection.

The Examiner correctly admits in the office action that Kozak and Jodoin do not specifically disclose a first interposer (or socket) between a component and a first electrical contact area on a first side of a substrate and a second interposer (or socket) between a capacitor plate and a second electrical contact area on a second side of the substrate. In an attempt to overcome the deficiencies of Kozak and Jodoin, the Examiner relies on Kabadi and Wisser in an attempt to show various features.

Claims 14-15 each depends from claim 13 and are each patentable over the Kozak-Kabadi-Wisser combination or the Jodoin-Kabadi-Wisser combination for at least the same reasons that claim 13 is patentable over the cited references, considered singly or in combination. Furthermore, each of the claims 14-15 distinguishes over the Kozak-Kabadi-Wisser combination and the Jodoin-Kabadi-Wisser combination by reciting additional features. Accordingly, claims 14-15 are each patentable over the Kozak-Kabadi-Wisser combination and the Jodoin-Kabadi-Wisser combination.

For the above reasons, Applicant requests reconsideration and withdrawal of the rejection under 35 U.S.C. §103.

New claims are being added and each new claim recites features that are not disclosed and are not suggested by the cited references, considered singly or in combination.

Accordingly, each of the new claims are patentable over the cited references, considered singly or in combination.

Applicant respectfully requests allowance of all pending claims.


If the undersigned attorney has overlooked a teaching in the cited reference that is relevant to the allowability of the claims, the Examiner is respectfully requested to specifically point out where such teachings may be found.

CONTACT INFORMATION

If the Examiner has any questions or needs any additional information, the Examiner is invited to telephone the undersigned attorney at (805)681-5078.

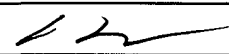
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